UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

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Assignee:

Samsung Electronics Co., Ltd.

Title:

Semiconductor Memory Device And Method Of Manufacturing For

Preventing Bit Line Oxidation

Serial No.:

09/008,525

Filed:

01/16/98

Examiner:

Not Assigned

Group Art Unit: 2811

Docket No.:

**AB-778 US** 

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San Jose, California March 21, 2000

JUL 11 6 2001

ASSISTANT COMMISSIONER FOR PATENTS Washington, D. C. 20231

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## INFORMATION DISCLOSURE STATEMENT WITH **CERTIFICATION UNDER § 1.97(e)(1)**

Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicant(s) wish to call the following documents (copies enclosed) to the attention of the Examiner. The documents were cited in the enclosed European Search Report for the corresponding European Application No. EP 98 30 0304.

A PTO form 1449 listing these documents is enclosed.

The relevance of any non-English language documents cited herein is explained in the accompanying copy of the Abstract.

Citation of the above documents shall not be construed as:

- 1. an admission that the documents are necessarily prior art with respect to the instant invention;
- 2. a representation that a search has been made, other than as described above; or

1

SERIAL NO.: 09/008,525

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3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

The undersigned hereby certifies in accordance with § 1.97(e)(1) that each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: ASSISTANT COMMISSIONER FOR PATENTS, Washington, D.C. 20231, on March 21, 2000.

Attorney for Applicants

3-21-00

Date of Signature

Respectfully submitted,

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Reg. No. 37,396

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